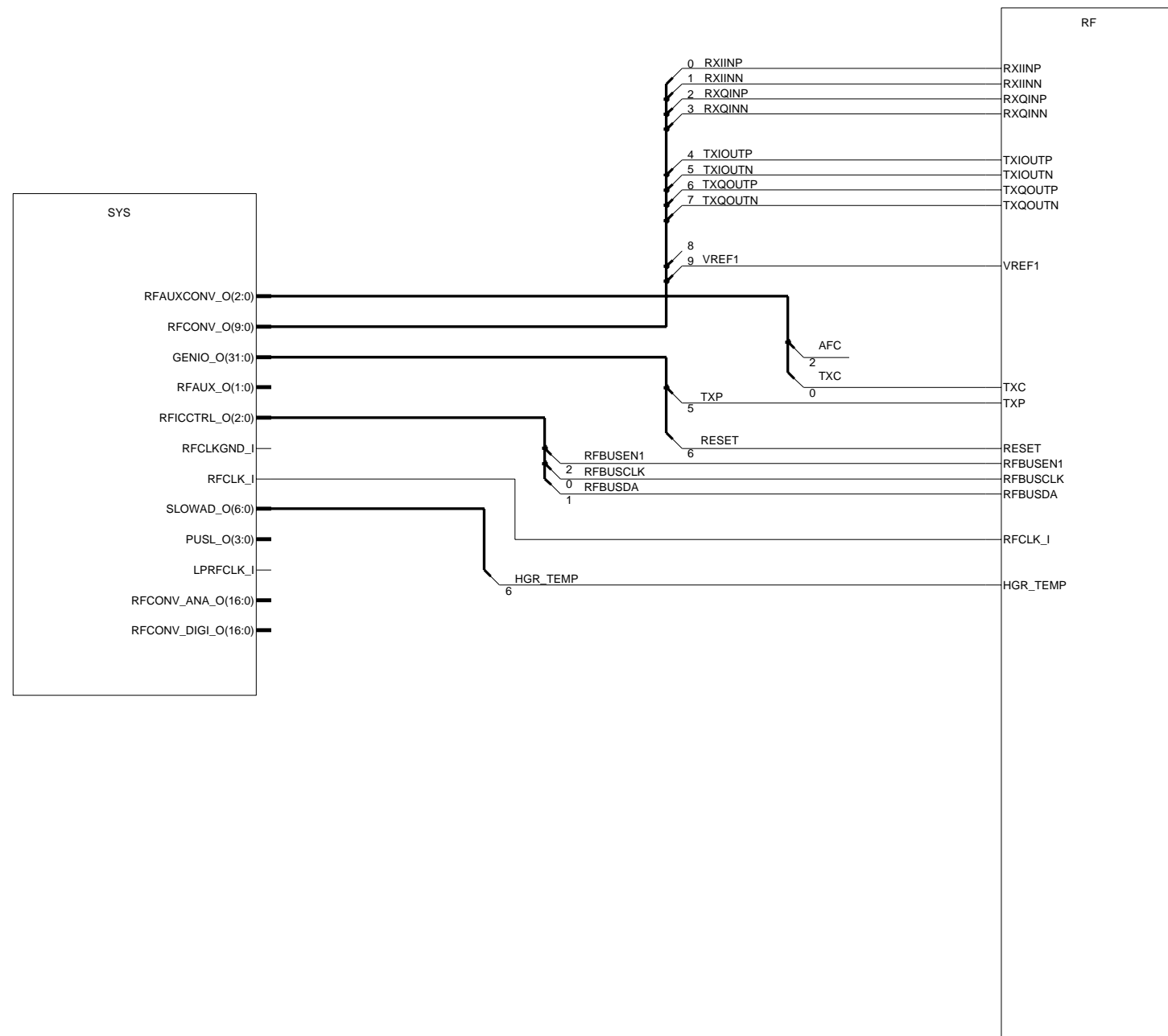


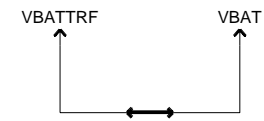
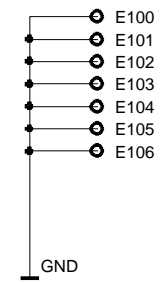
10 - SCHEMATICS

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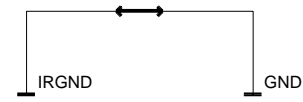
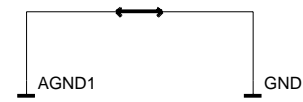
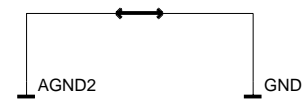
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Title: Top level schematic

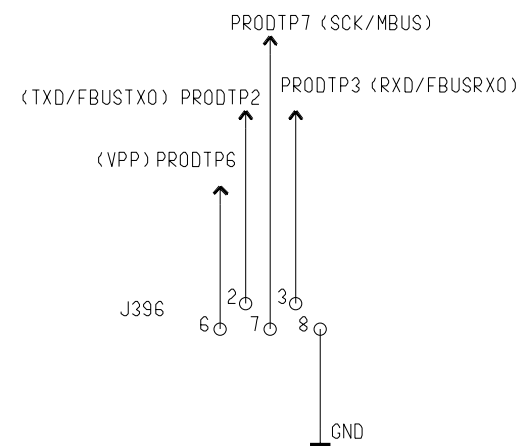




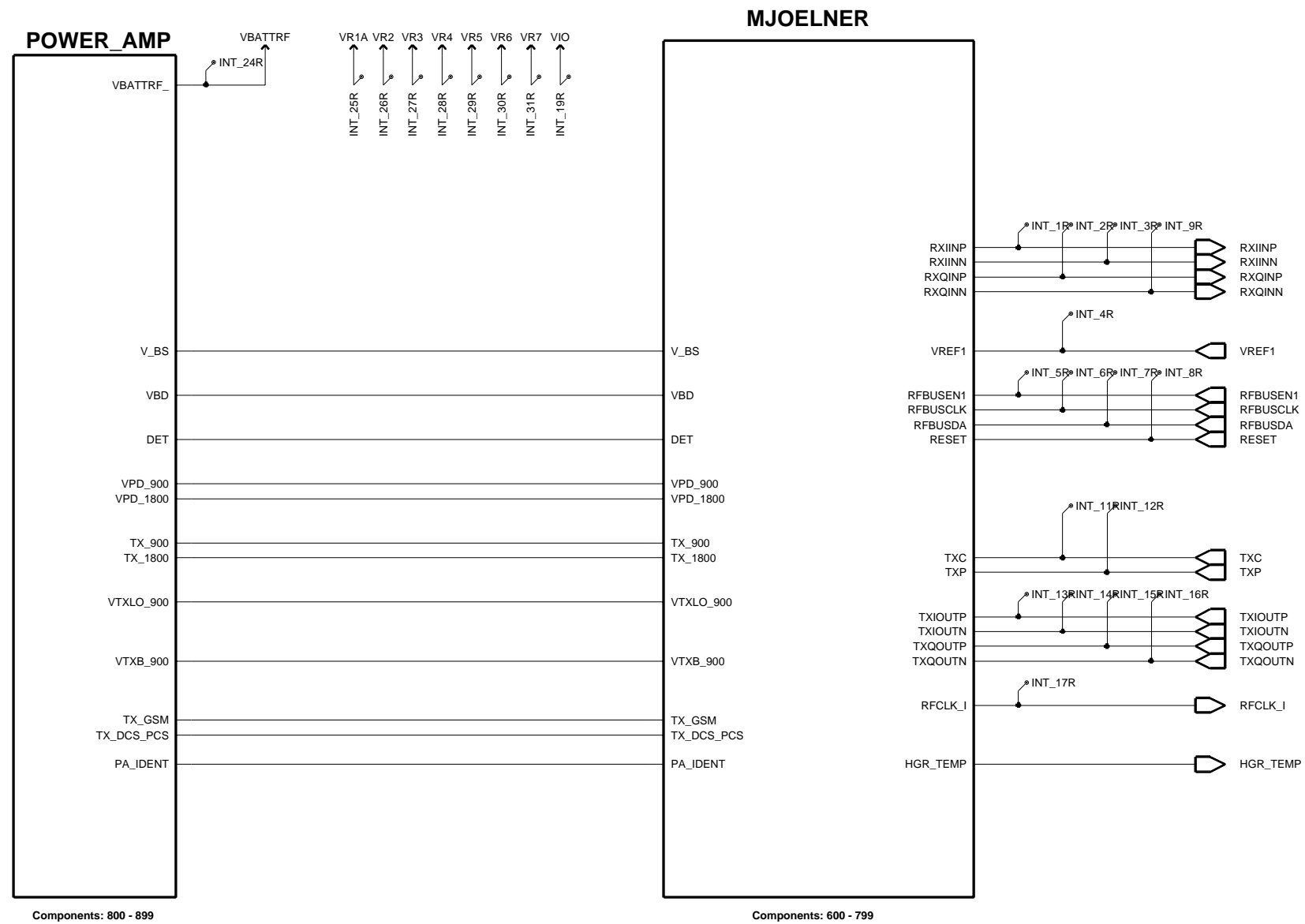
Title: DCT4 Common Baseband Schematic (Top level 2)



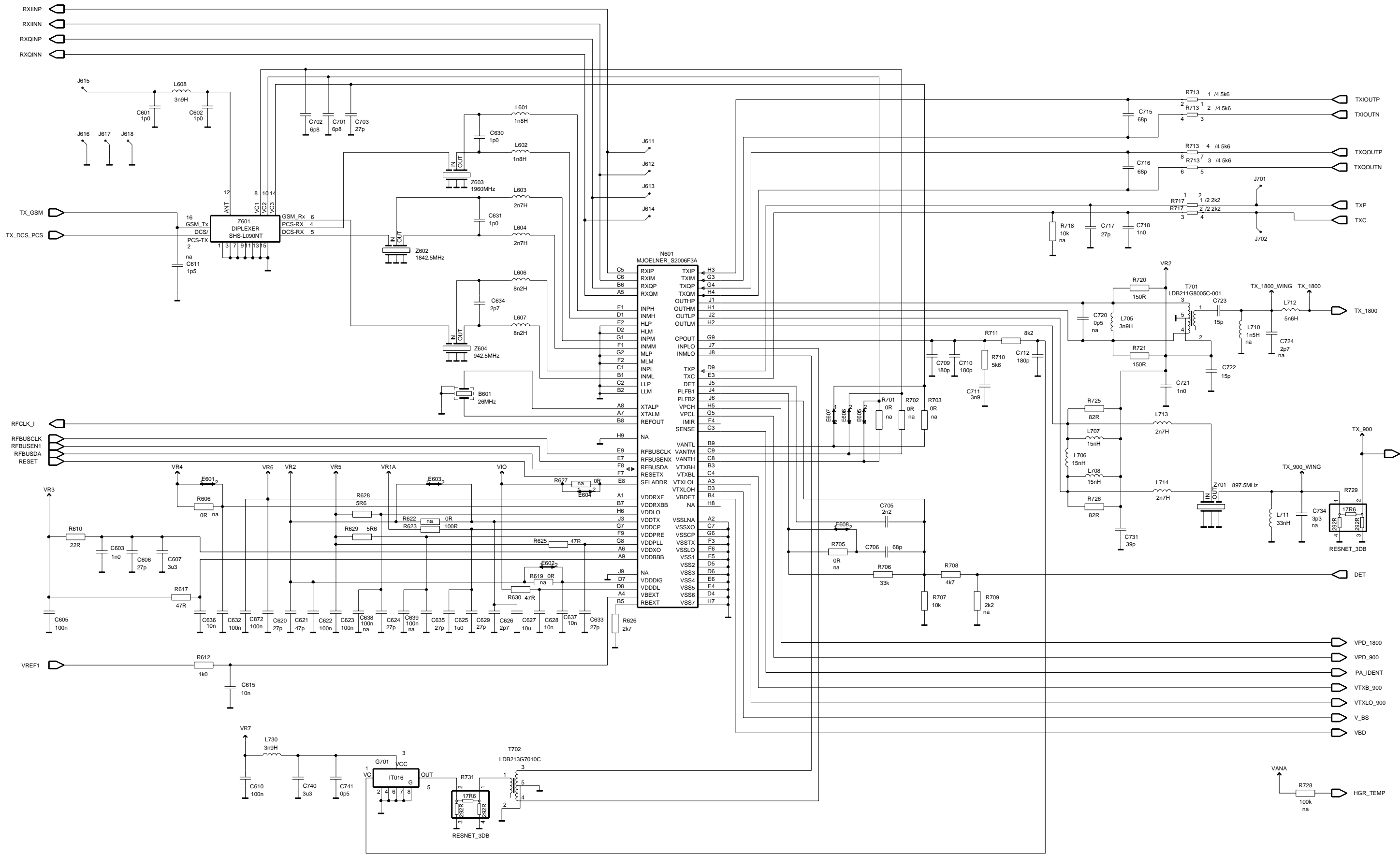
Title: 5 pin Production Test Pattern



Title: RF top sheet schematic

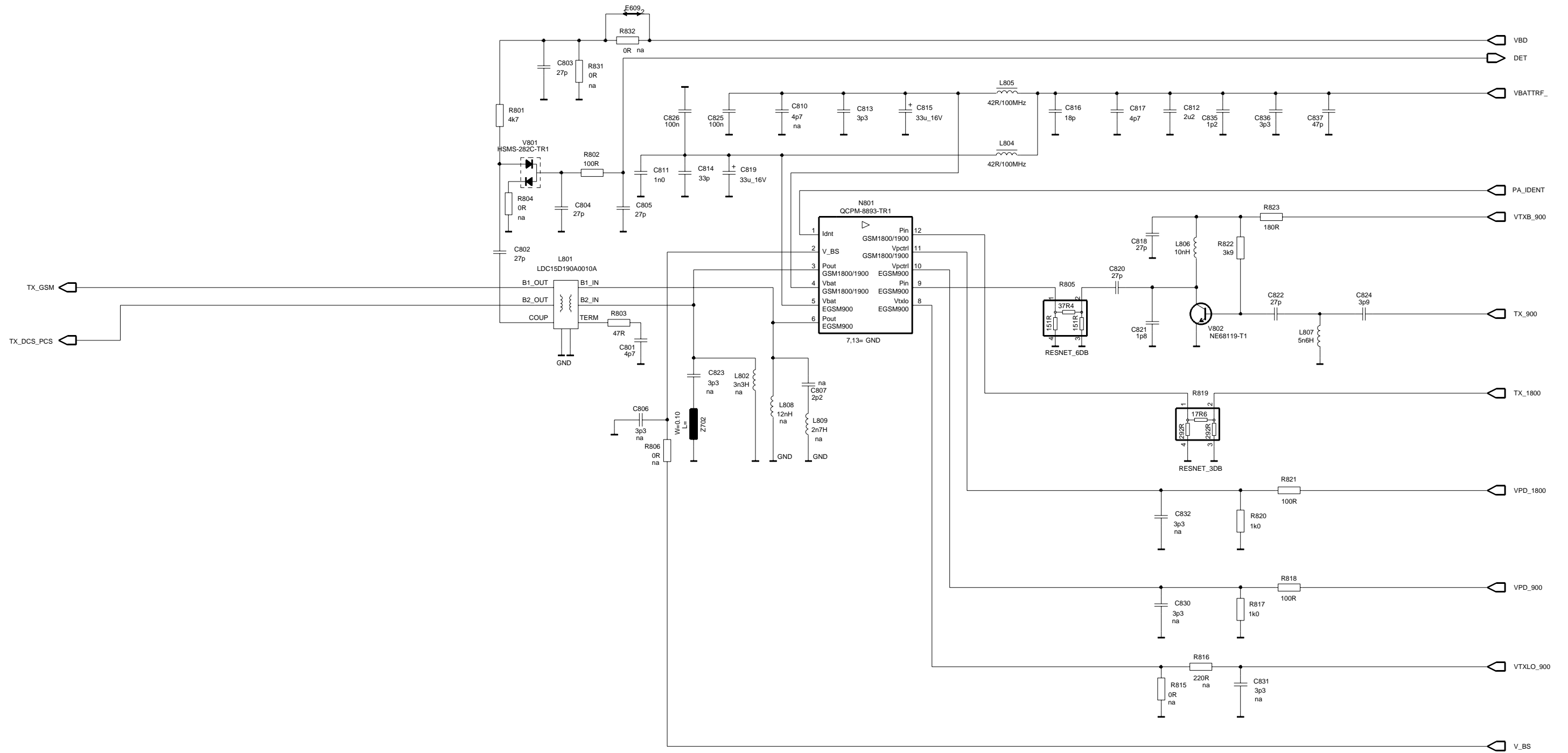


Title: Mjoelner



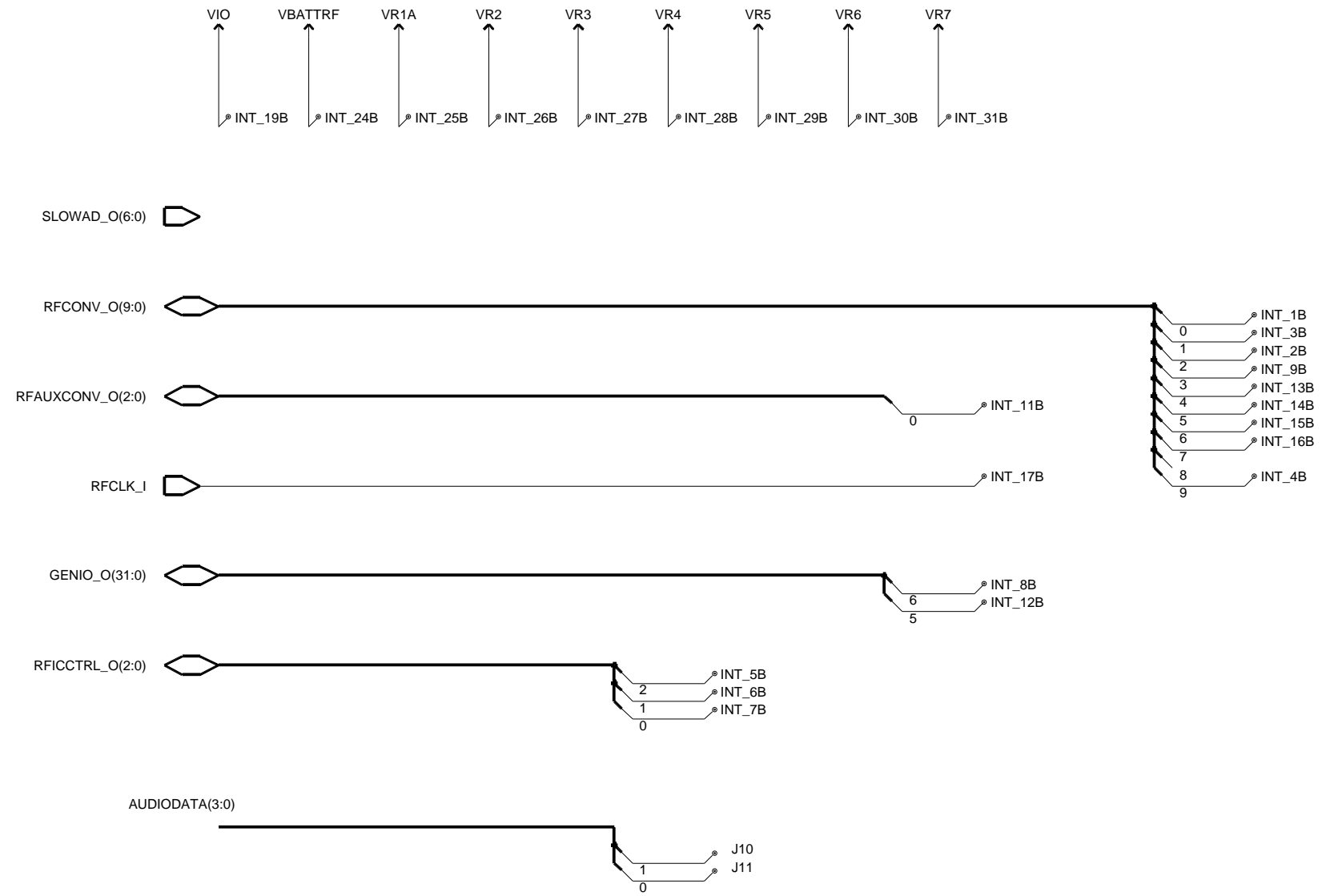
na = not assembled

Title: Power amplifier

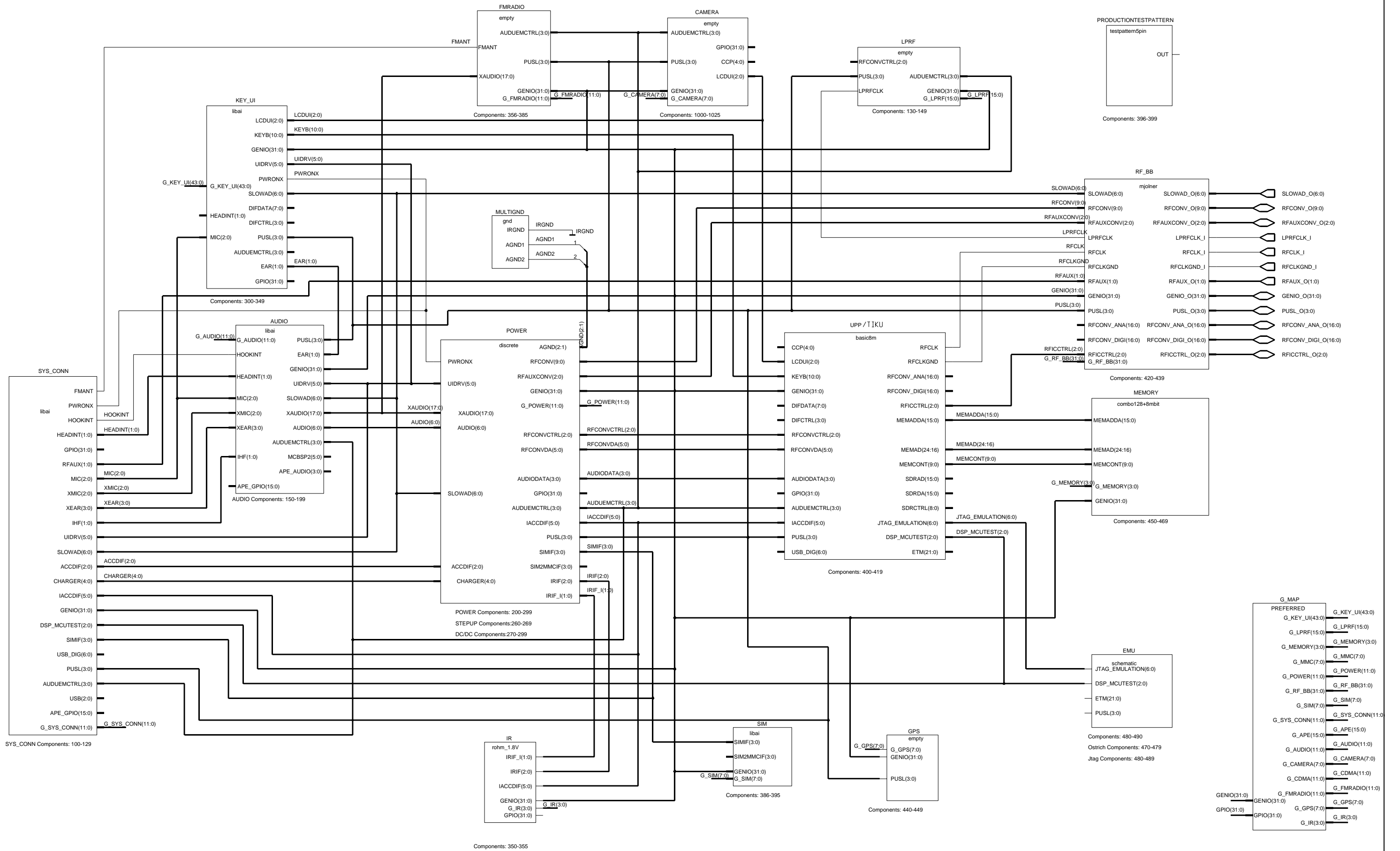


na = not assembled

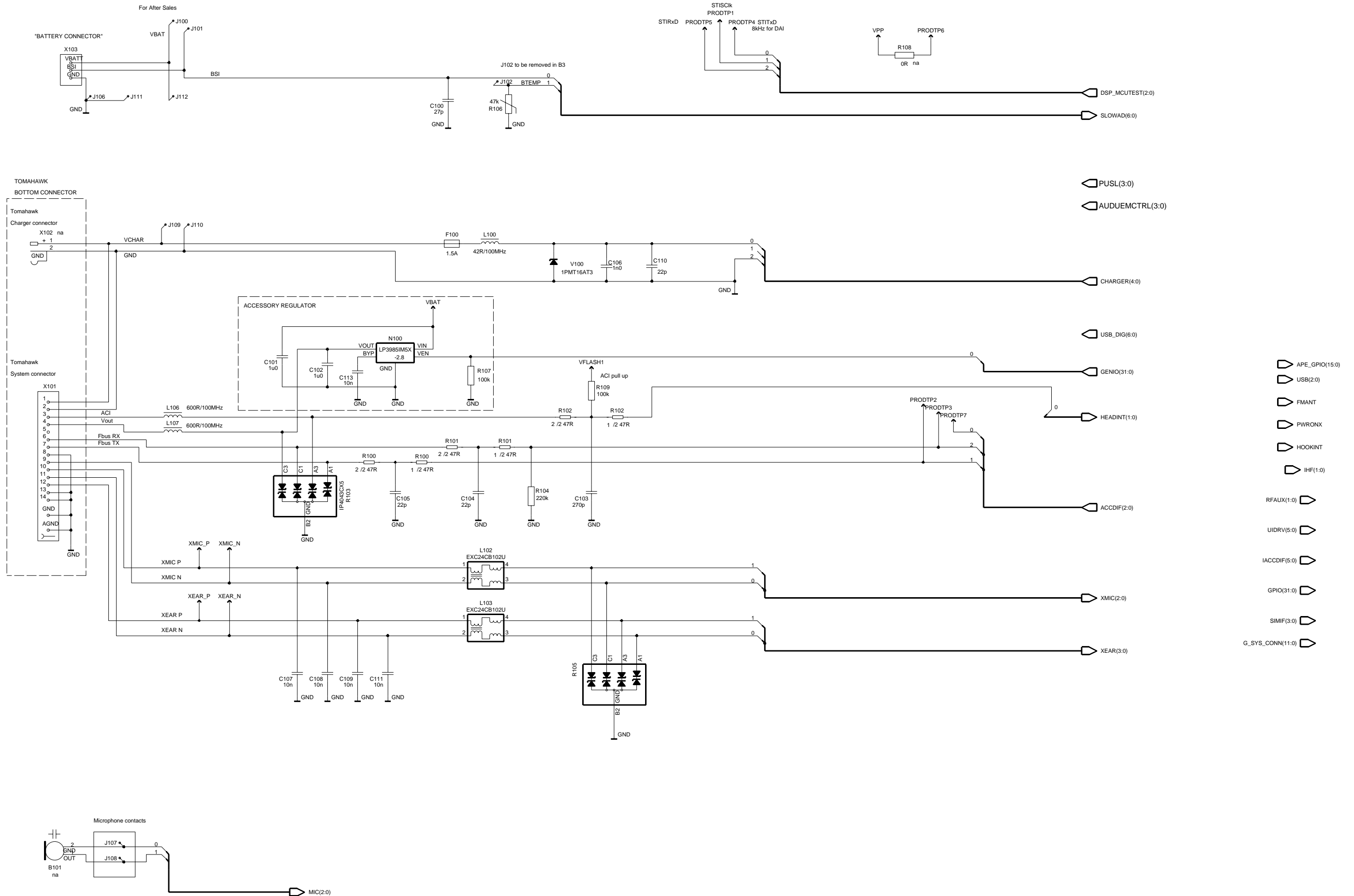
Title: BB RF interface components



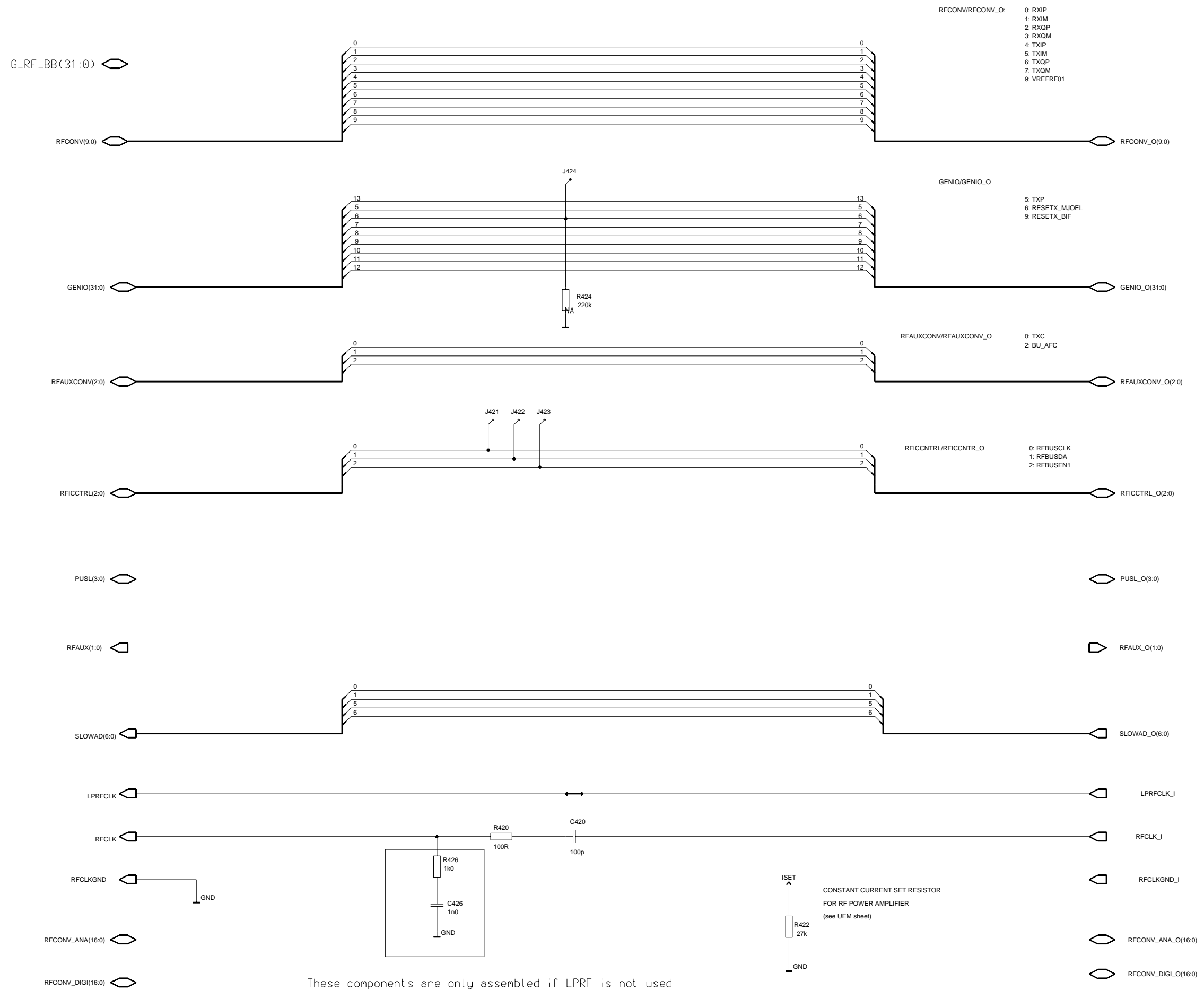
Title: DCT4 common BB Schematic (top level)



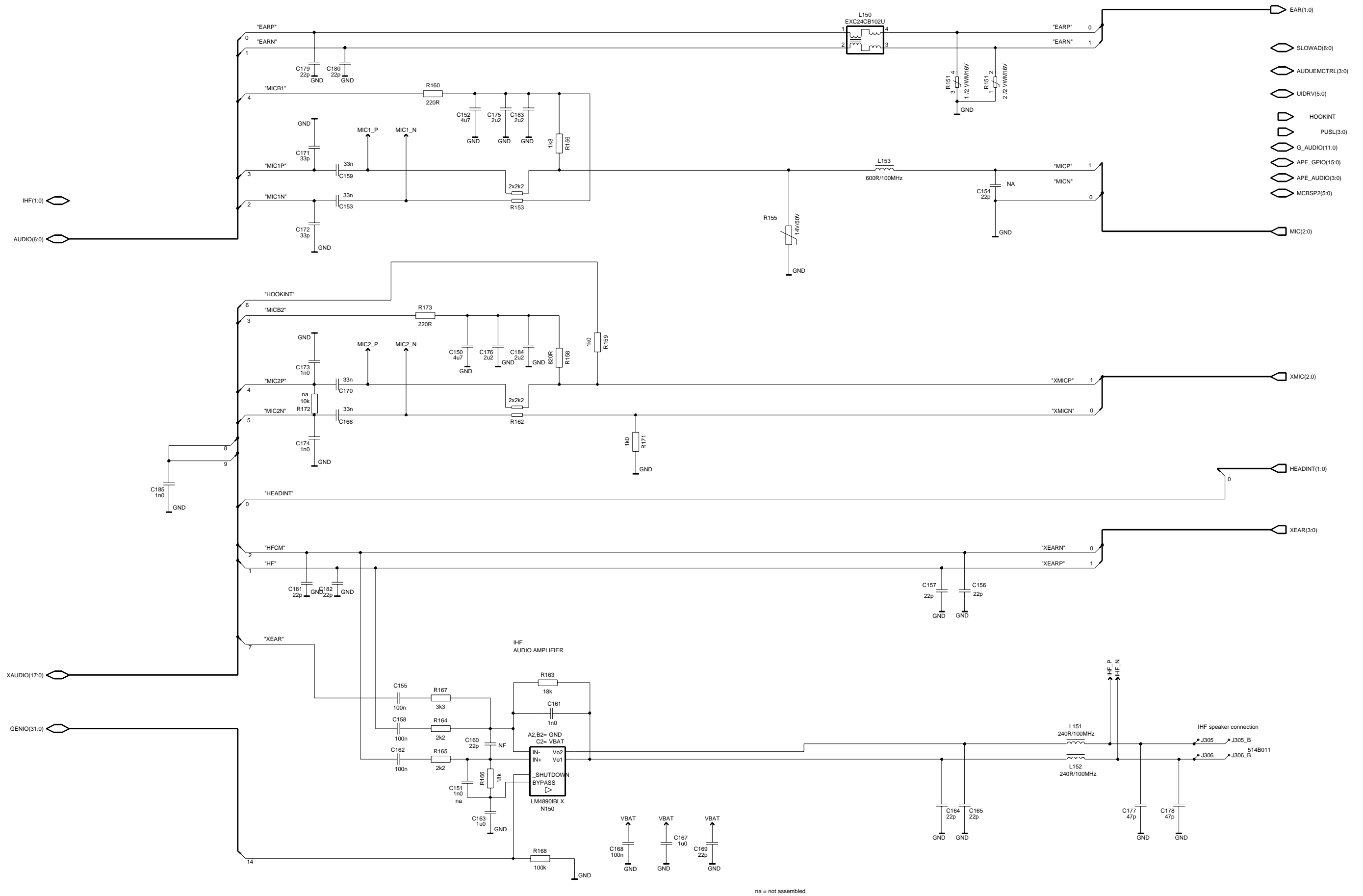
Title: RH-4 System connector

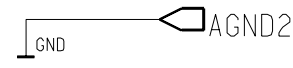
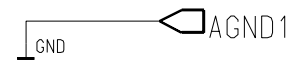
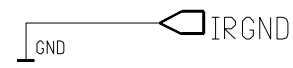


Title: GSM RF BB interface



Title: RH-4 Audio

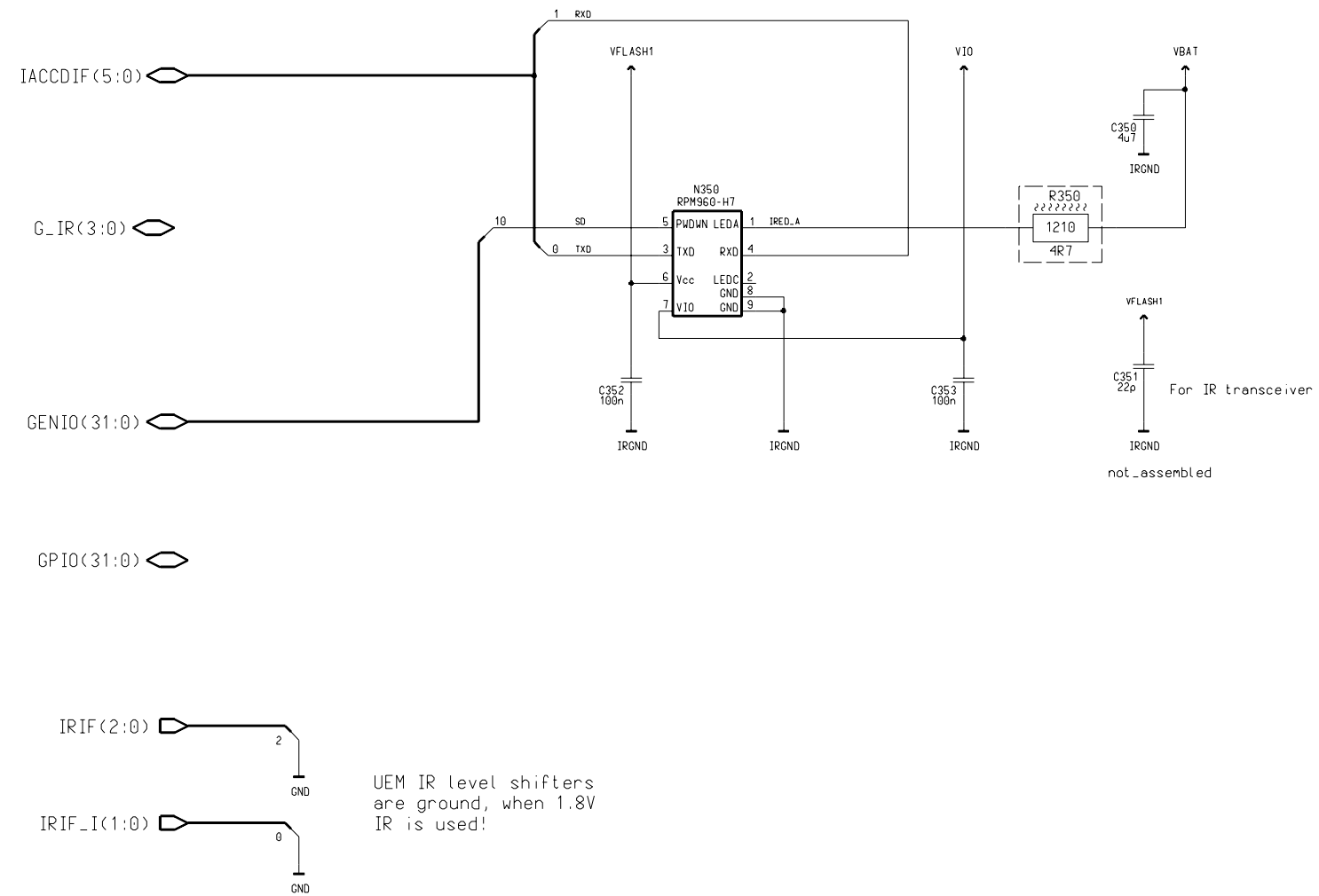




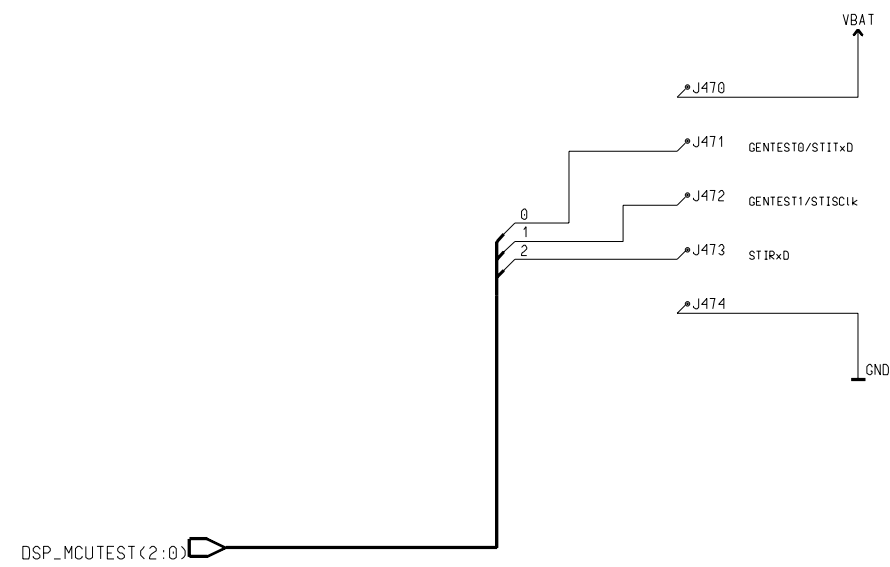
Title: GENIO and GPIO connection block



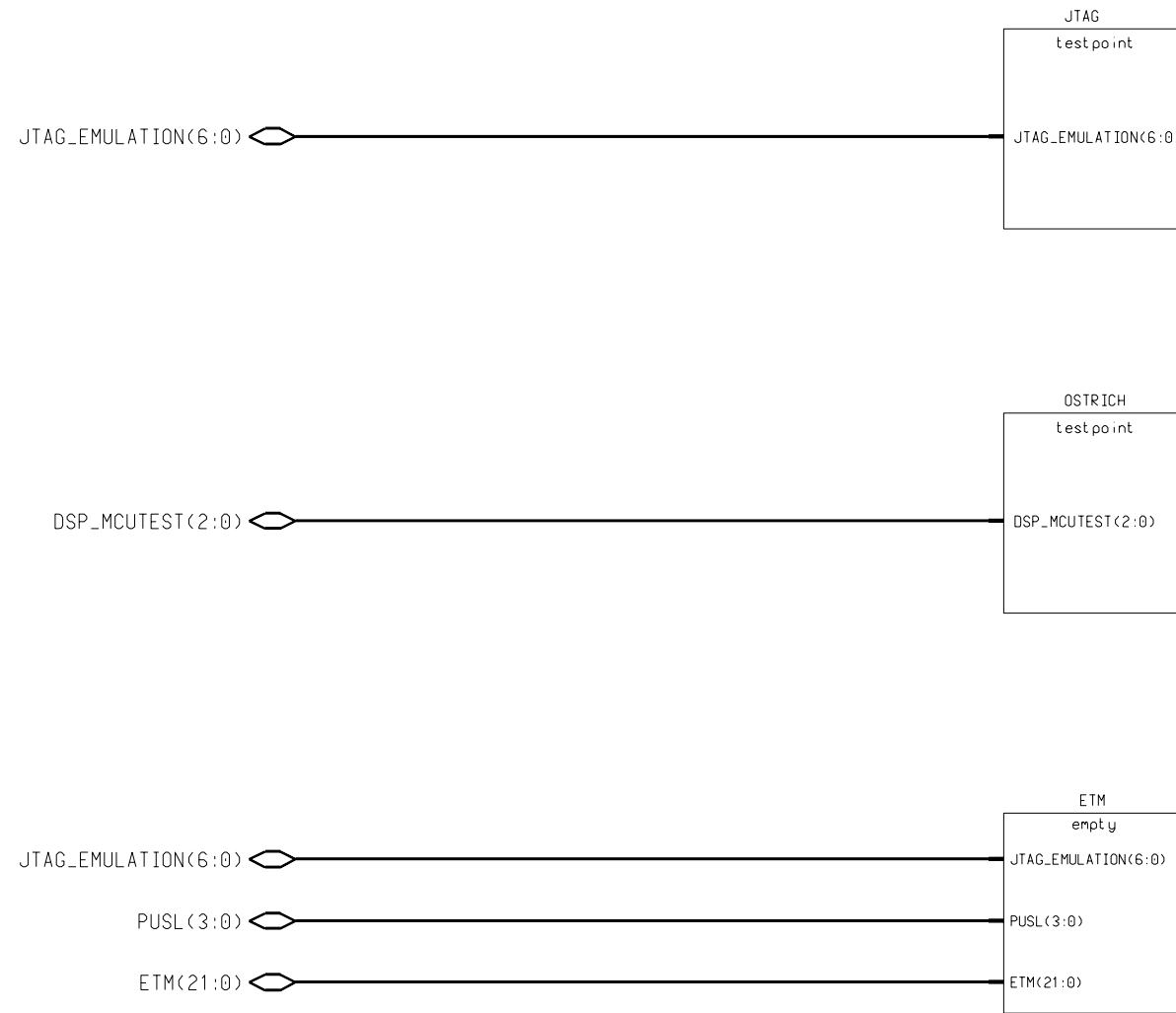
Title: Rohm IR module 1.8V



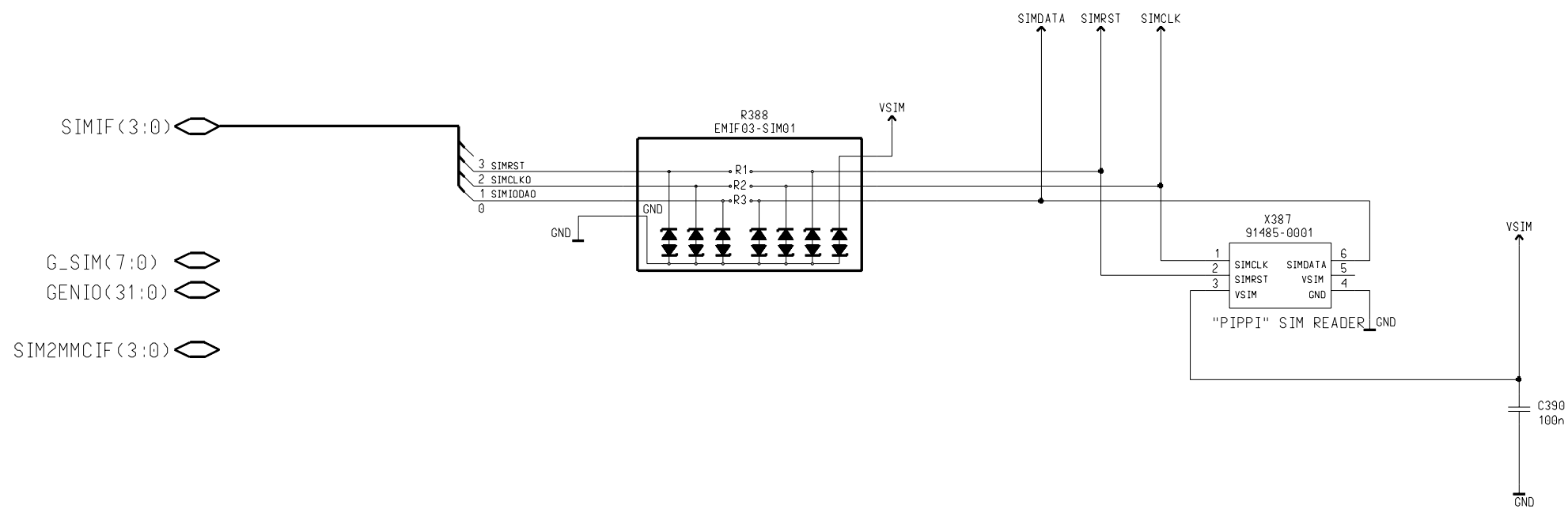
Title: Testpoints based Ostrich interface



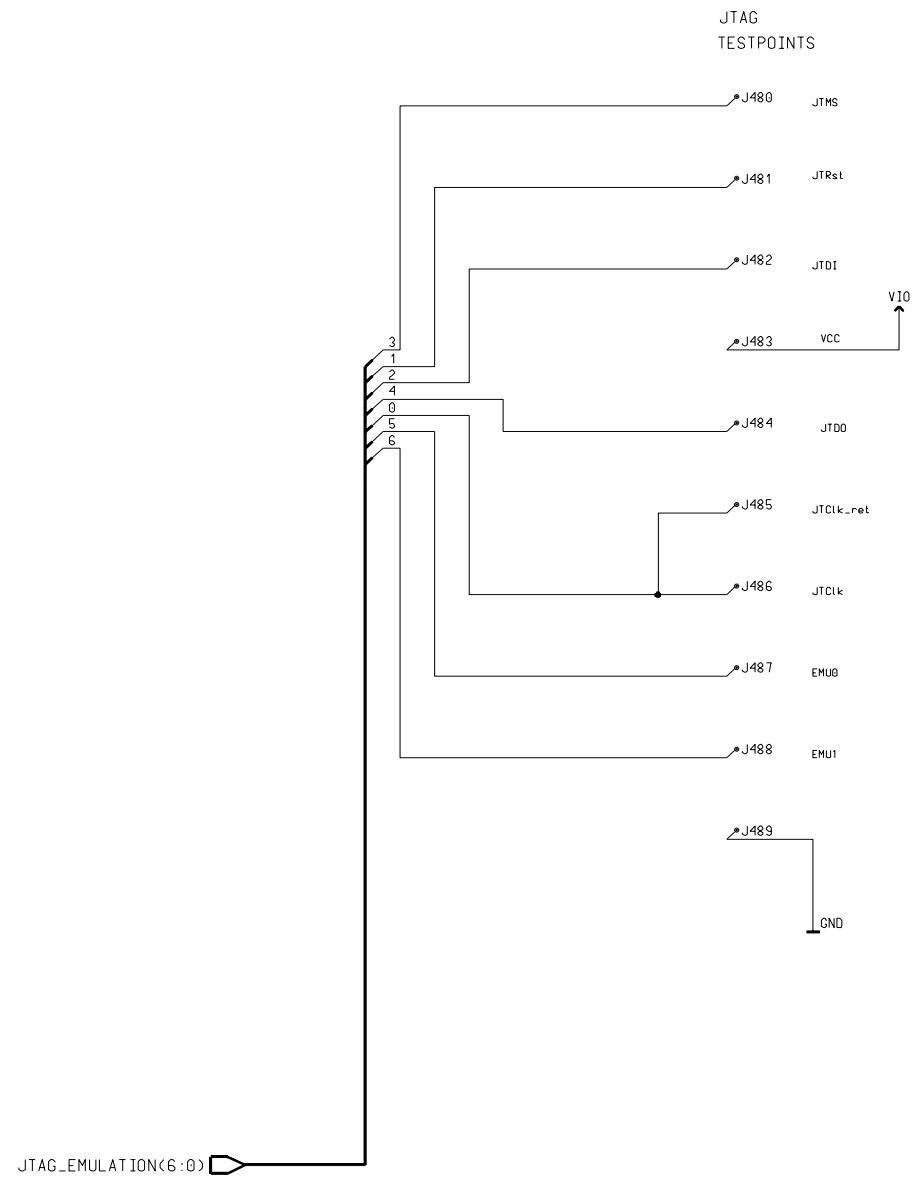
Title: Test and emulator interface



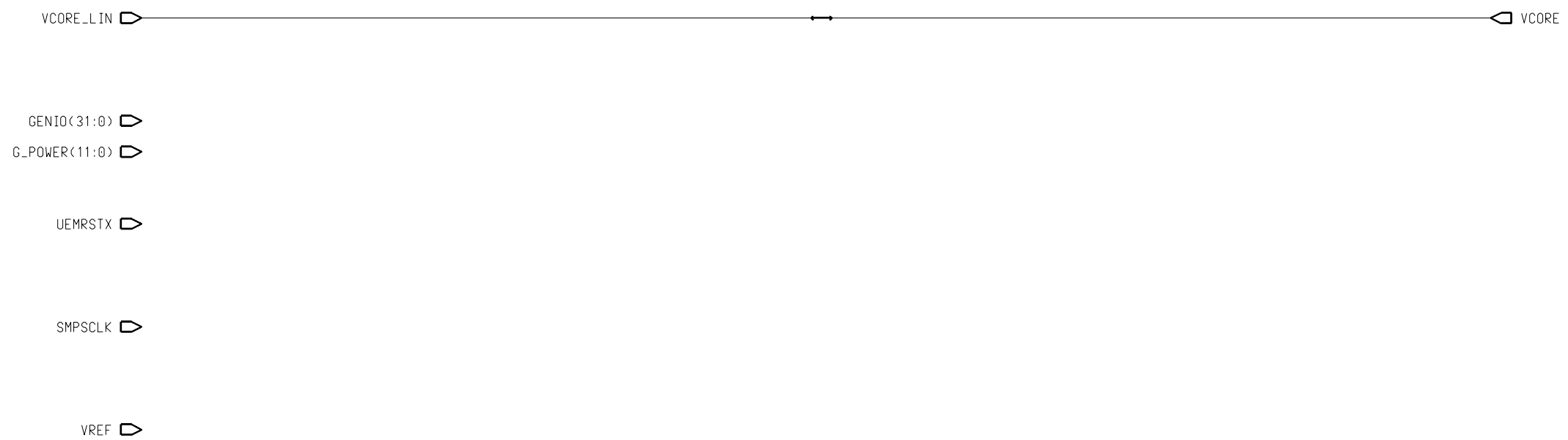
Title: SIM reader



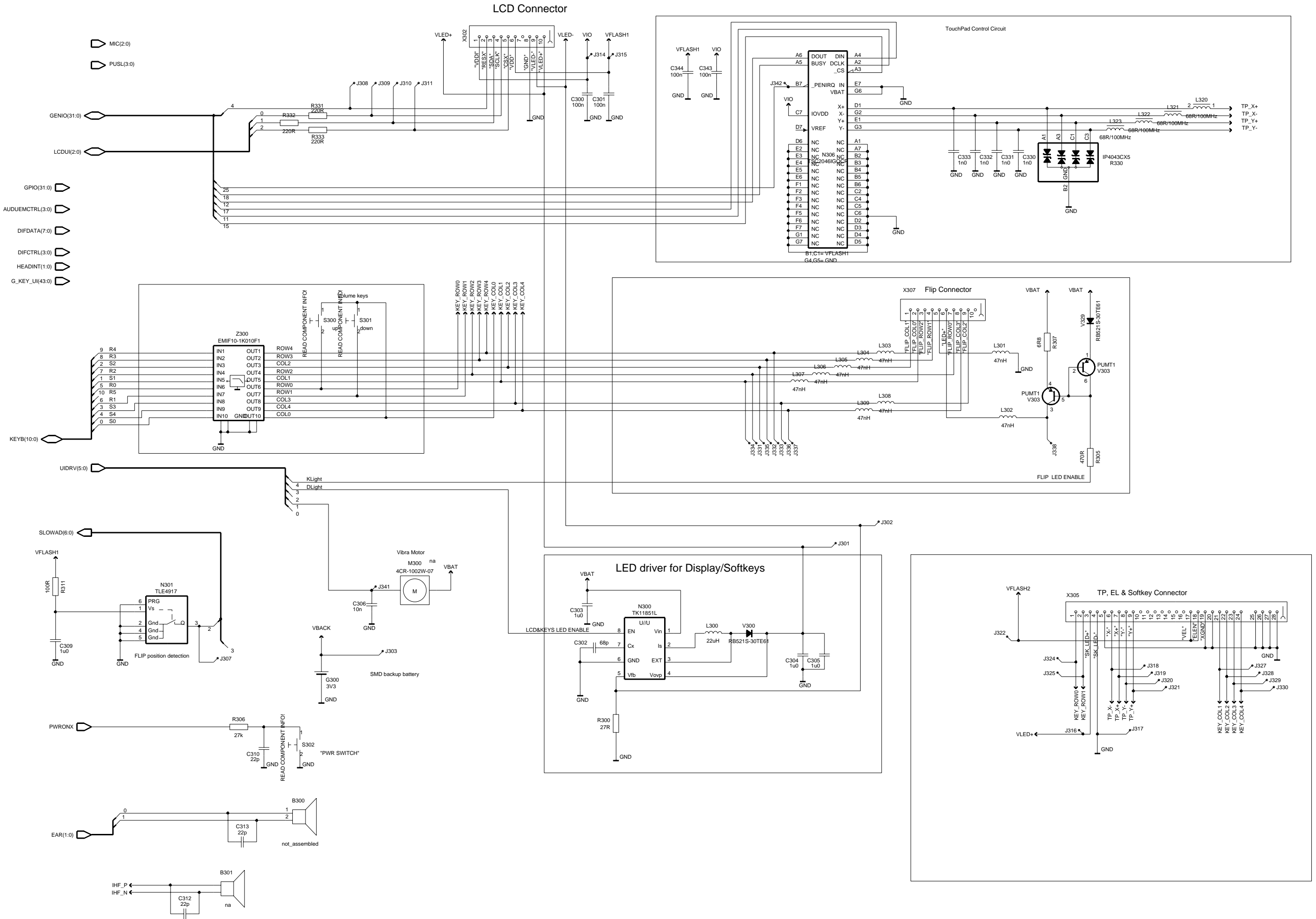
Title: Testpoints for JTAG emulator



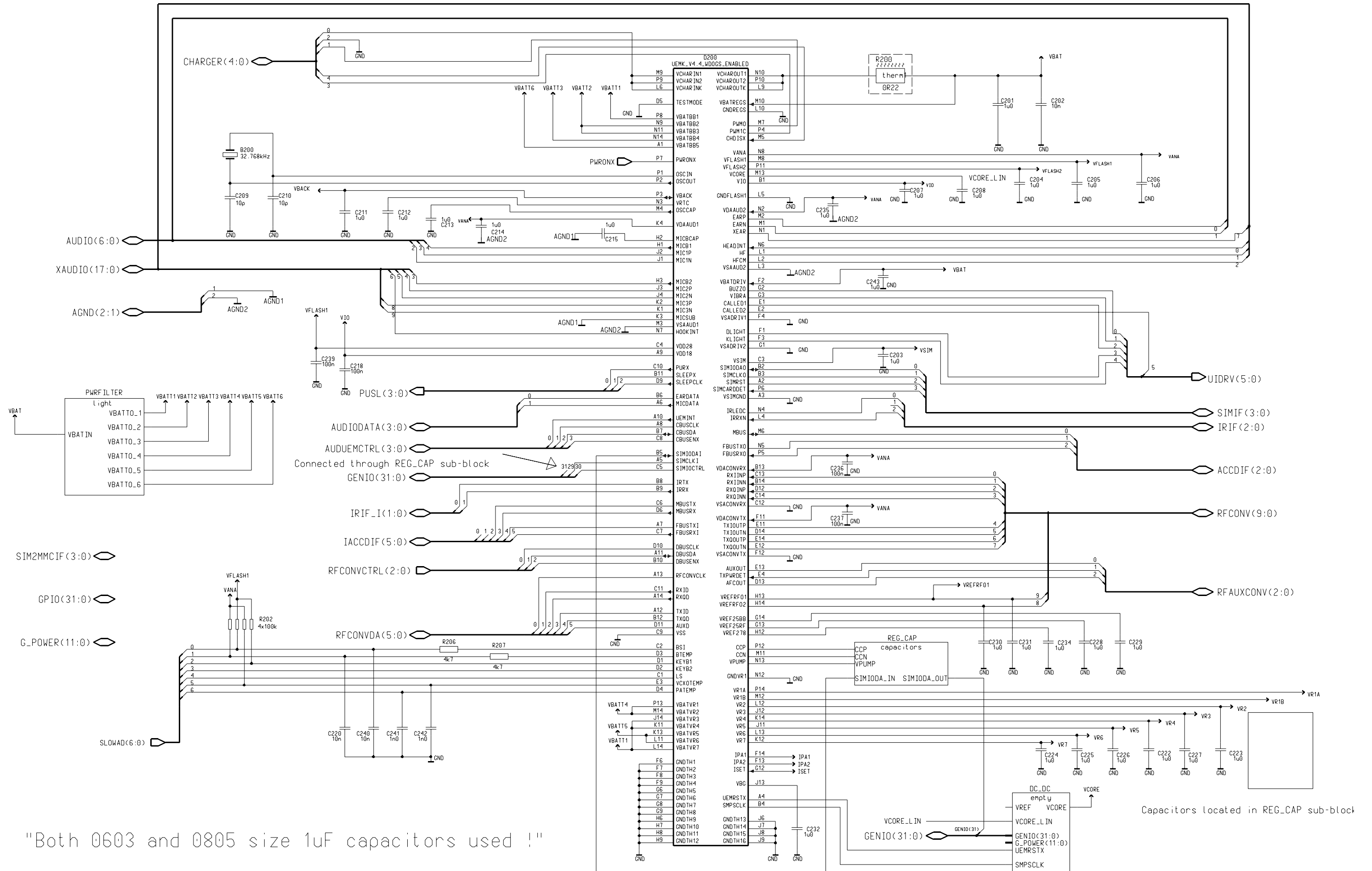
Title: DC/DC convertor



Title: RH-4 User interface



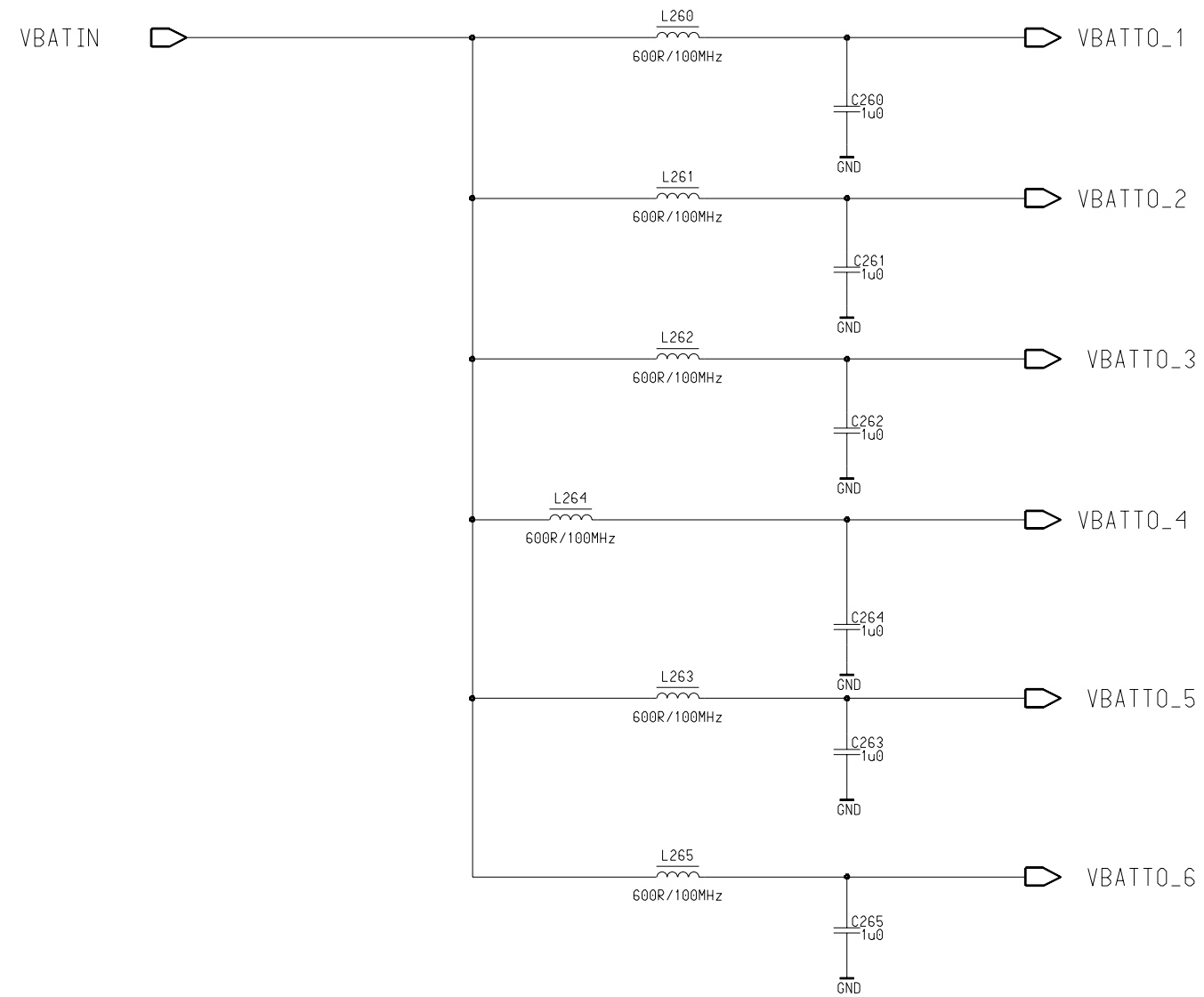
Title: Discrete power management



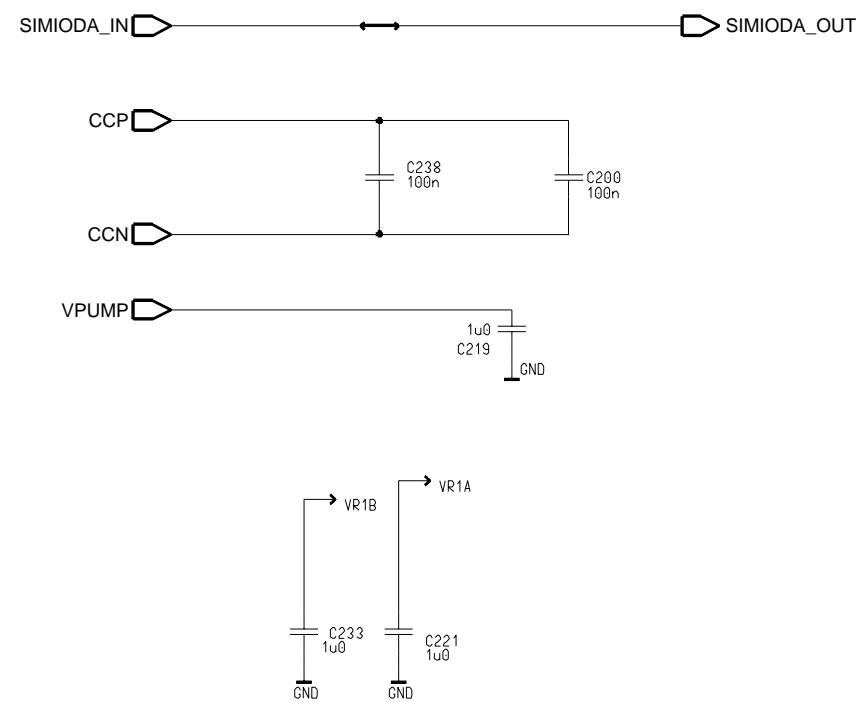
"Both 0603 and 0805 size 1uF capacitors used !"

Capacitors located in REG_CAP sub-block

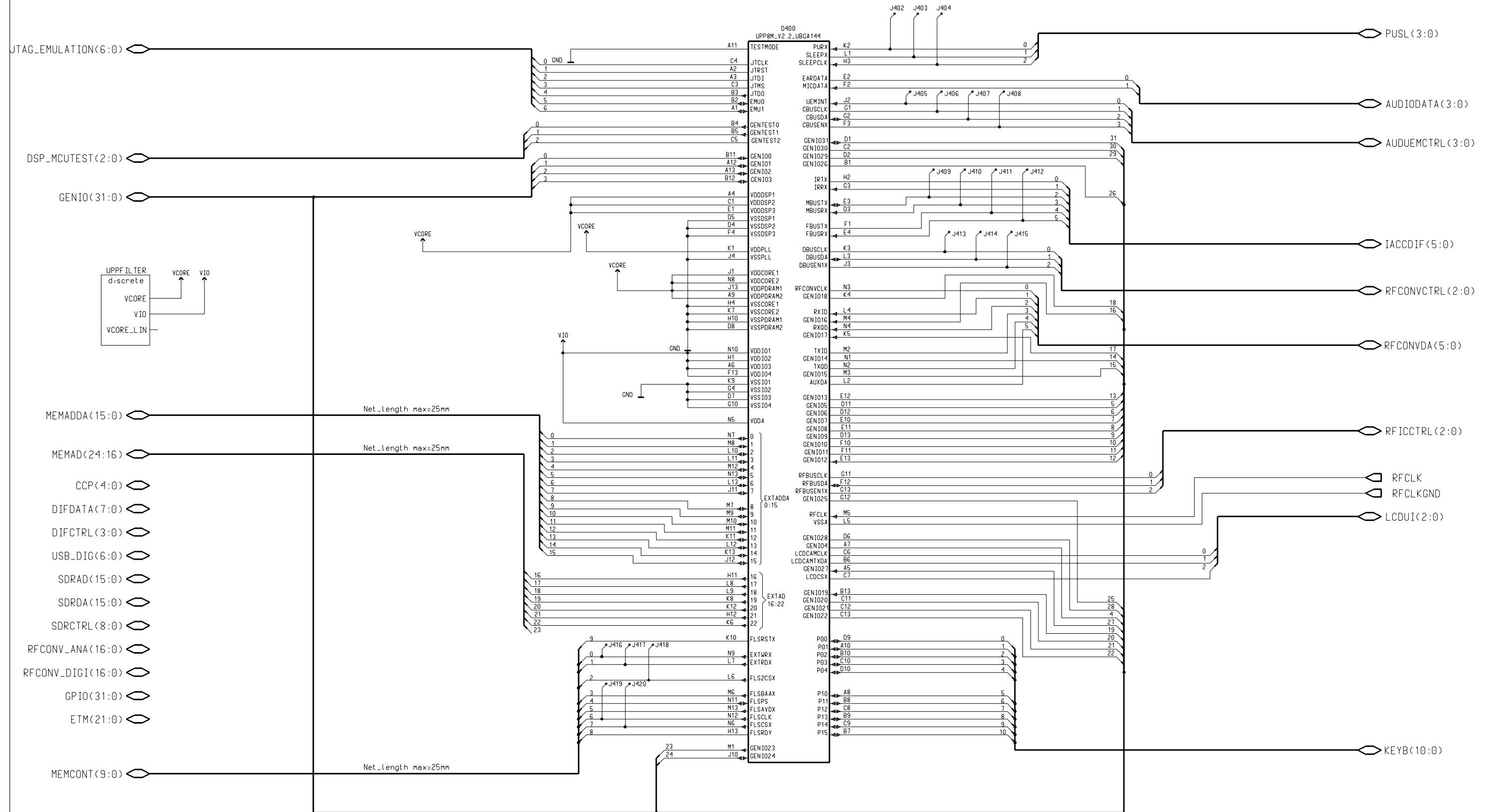
Title: Light filtering



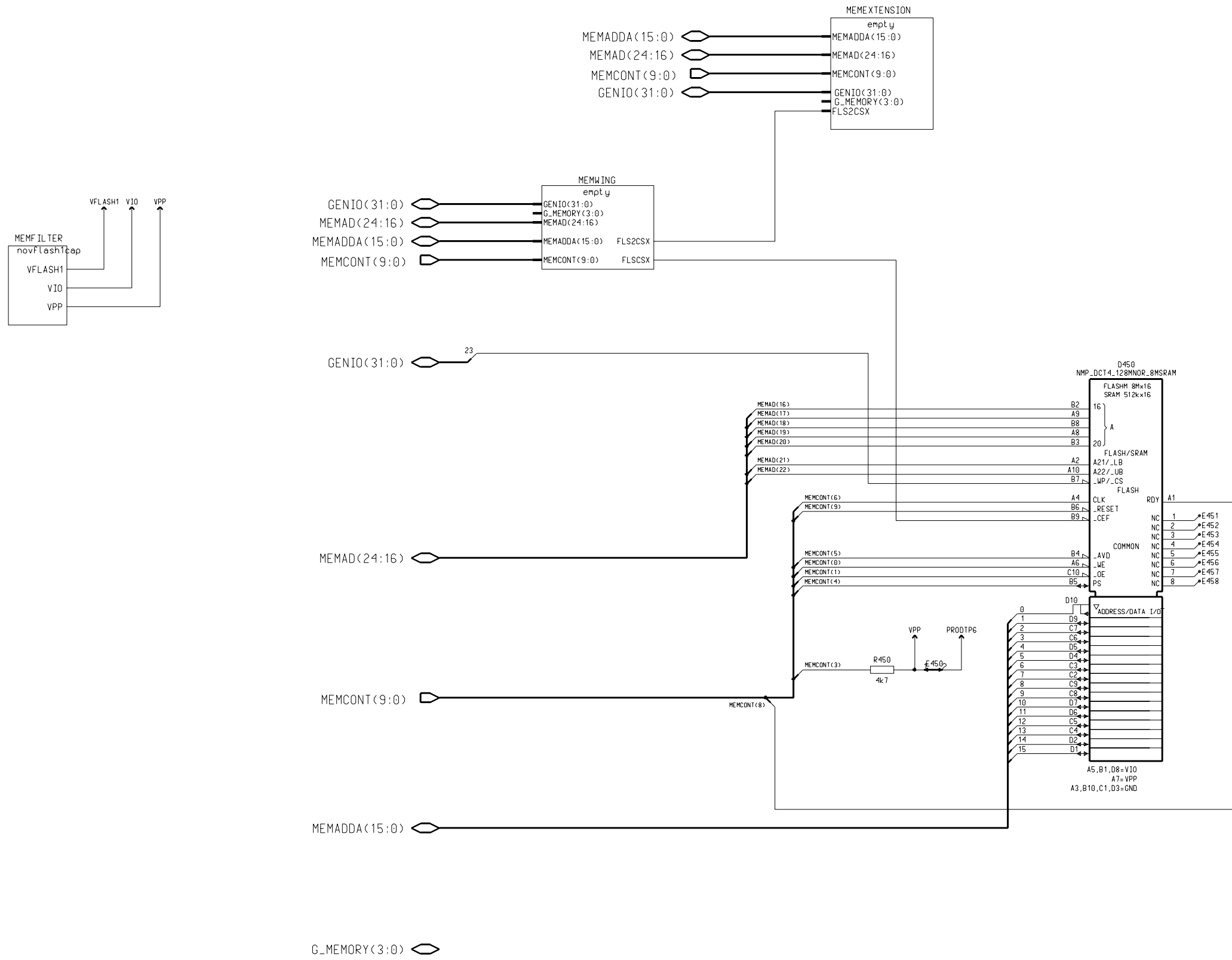
Title: Old power discrete users



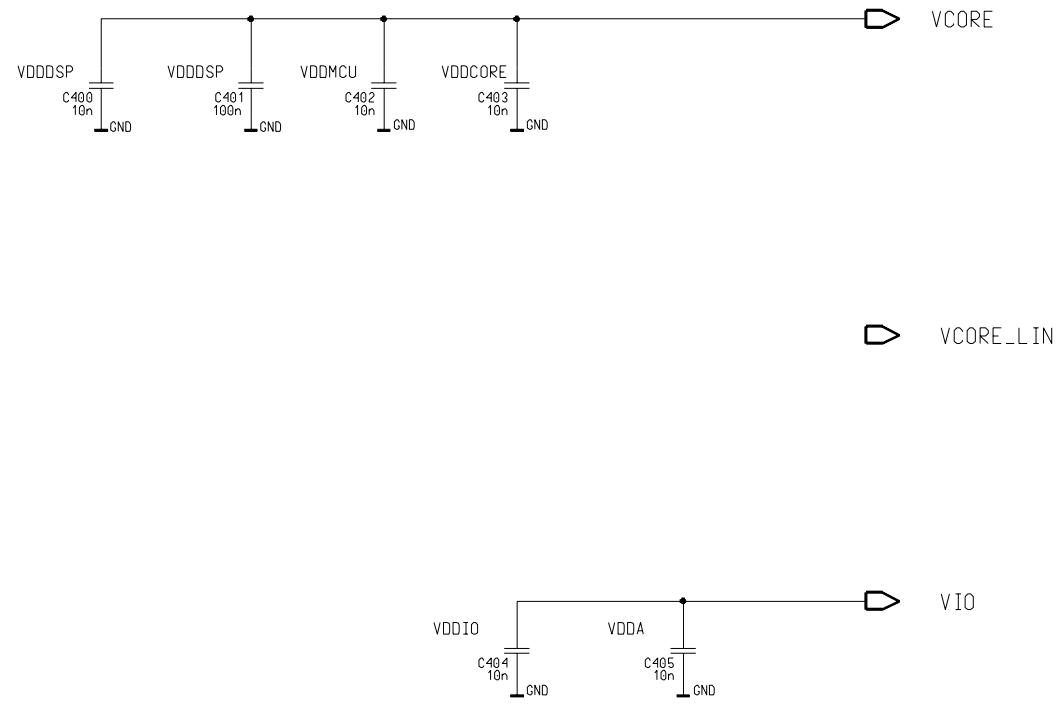
Title: UPP 8M implementation



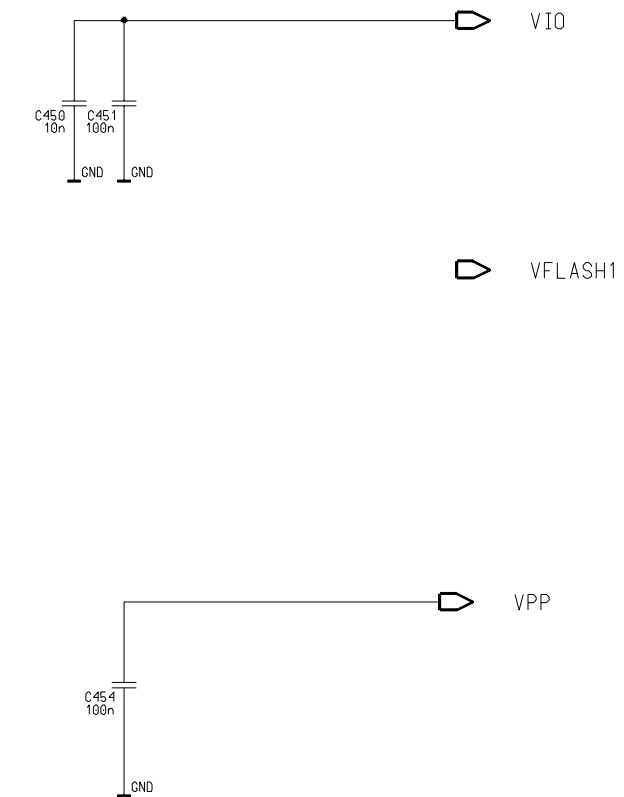
Title: Combo memory 128 +8 Mbit



Title: Discrete decoupling capacitors for UPP



Title: Discrete capacitors for memory without VFlash 1



Title: Empty wing sheet

MEMADDA<15:0> ◁

MEMAD<24:16> ◁

GENIO<31:0> ◁

G_MEMORY<3:0> ◁

